

ABSTRACT

Disclosed herein is a driver circuit for a liquid crystal panel and liquid crystal display using the same. The driver circuit includes a gate and data driving units, a determination unit, and a
5 switching unit. The gate and data driving units apply signal voltages to gate and data lines of a liquid crystal panel. The determination unit determines whether horizontal or vertical synchronization signals are input from the outside, and performs a normal mode or power saving mode depending upon determination results. The switching unit shuts off power to the data and gate driving units depending upon the determination results.